

1-Line, Bi-directional, Transient Voltage Suppressor



SOD-323

Features

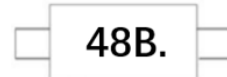
- Ultra small package
- Stand-off voltage: 48V Max
- Transient protection for each line according to IEC61000-4-2(ESD): $\pm 30\text{kV}$ (contact)
- IEC61000-4-5(surge): 4A (8/20 μs)
- Ultra-low capacitance: $C_J = 10\text{ pF}$ typ
- Low leakage current
- Low clamping voltage
- RoHS Compliant

Applications

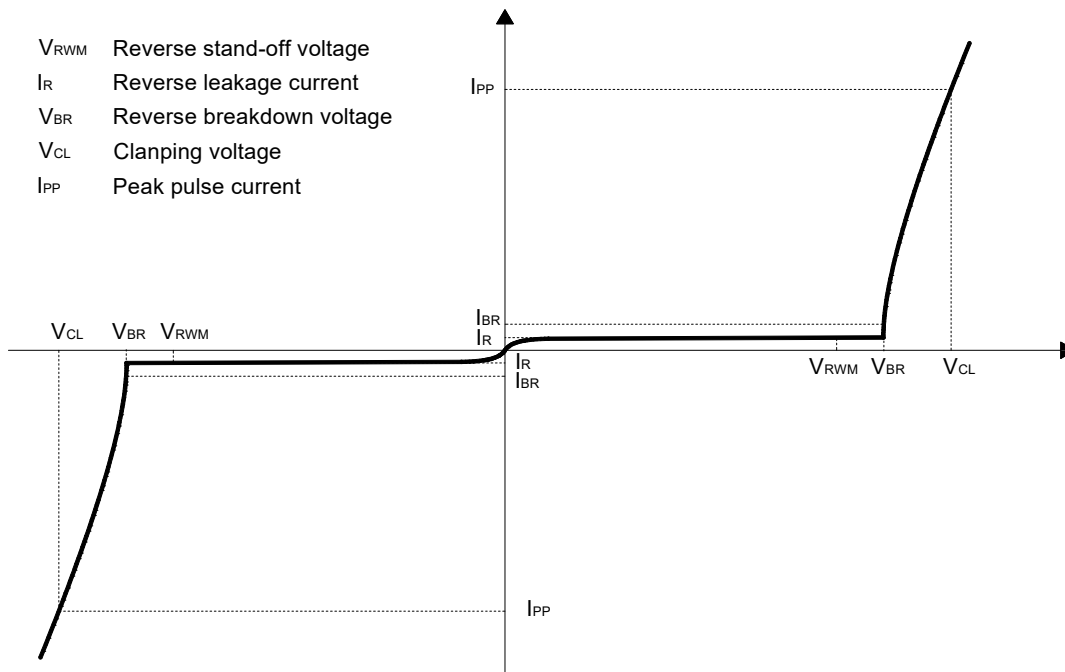
- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- Peripherals
- Pagers Peripherals
- Desktop and Servers

Mechanical Characteristics

- Package: SOD-323
- Case Material: "Green" Molding Compound.
- Marking Information: See Below



■Definitions of electrical characteristics





ESD48VD3B

■Absolute Maximum Ratings (Ta=25°C unless otherwise specified)

PARAMETER	SYMBOL	Rating	UNIT
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	380	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	4	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	KV
ESD according to IEC61000-4-2 contact discharge		± 30	KV
Junction temperature	T_J	125	°C
Storage temperature	T_{STG}	-55~150	°C

■Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				48
Reverse breakdown voltage	V_{BR}	V	$I_{BR} = 1mA$	50		61
Reverse leakage current	I_R	nA	$V_{RWM} = 48V$			500
Clamping voltage ³⁾	V_{CL}	V	$I_{PP} = 1A, t_p = 8/20\mu s$			75
		V	$I_{PP} = 4A, t_p = 8/20\mu s$			95
Junction capacitance	C_J	pF	$V_R = 0V, f = 1MHz$		10	15

(1). TLP parameter: $Z_0 = 50\Omega$, $t_p = 100ns$, $t_r = 2ns$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

(2). Contact discharge mode, according to IEC61000-4-2.

(3). Non-repetitive current pulse, according to IEC61000-4-5.

■Ordering Information (Example)

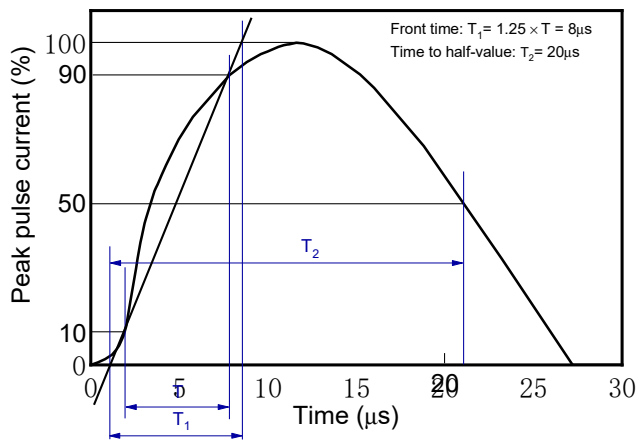
PREFERED P/N	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESD48VD3B	Approximate 4	3000	30000	120000	Tae& reel



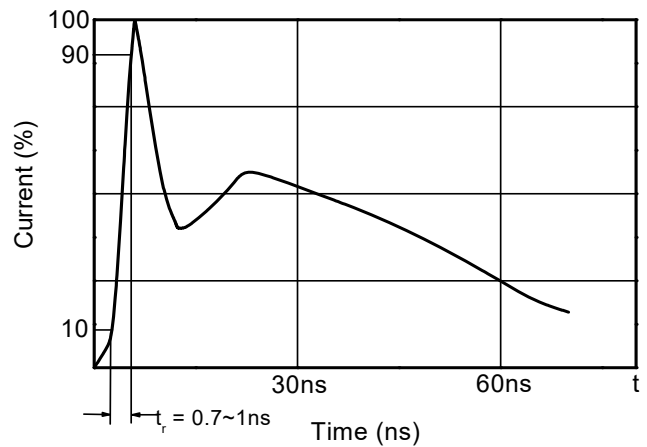
ESD48VD3B

■ Typical Performance Characteristics (Ta=25°C unless otherwise Specified)

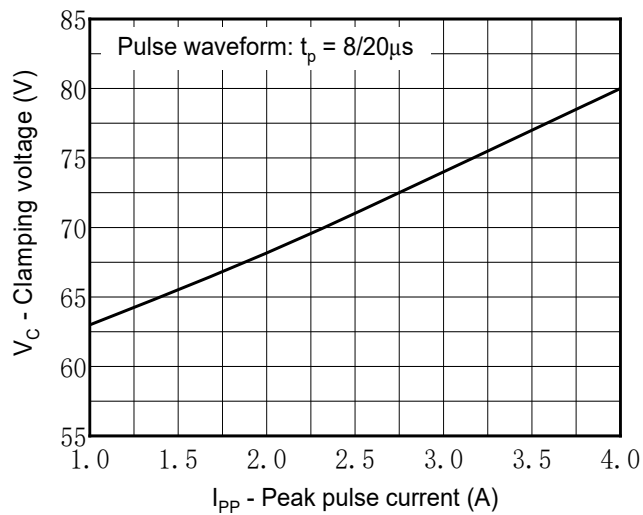
8/20μs waveform per IEC61000-4-5



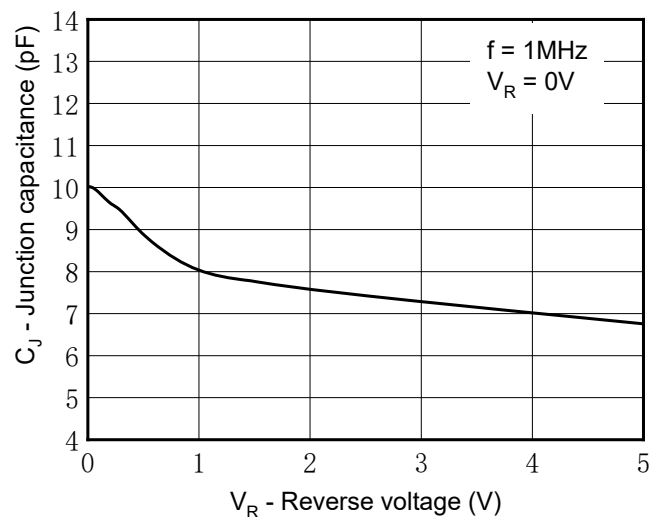
Contact discharge current waveform per IEC61000-4-2



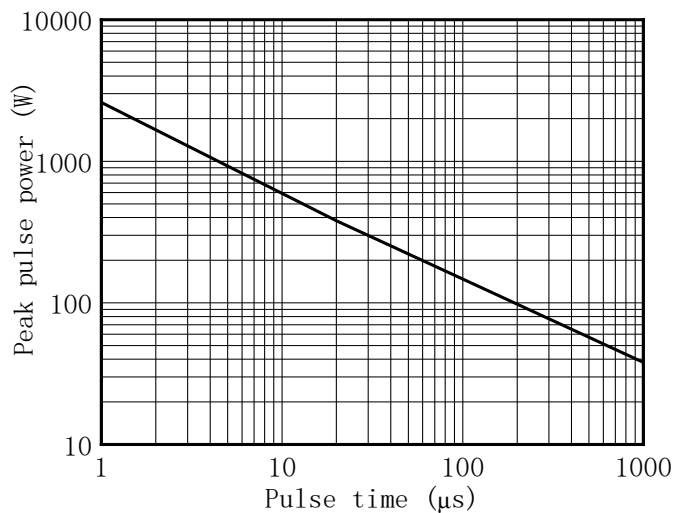
Clamping voltage vs. Peak pulse current



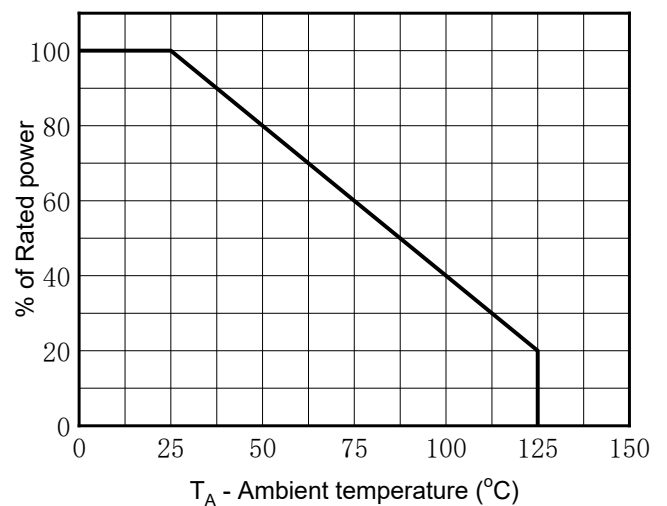
Capacitance vs. Reverse voltage



Non-repetitive peak pulse power vs. Pulse time

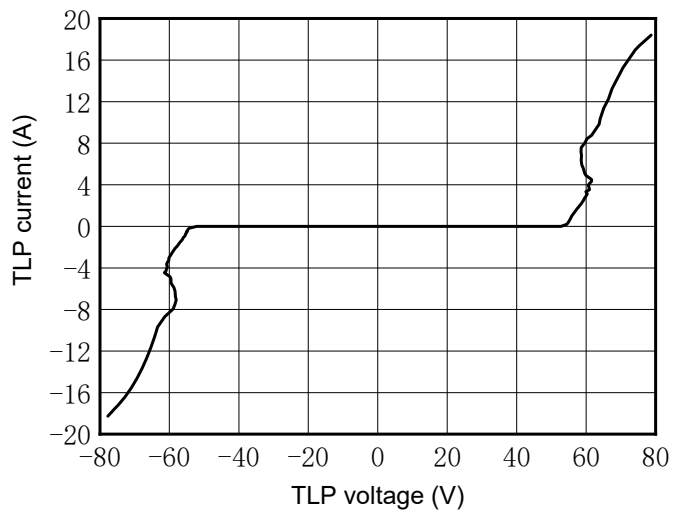


Power derating vs. Ambient temperature





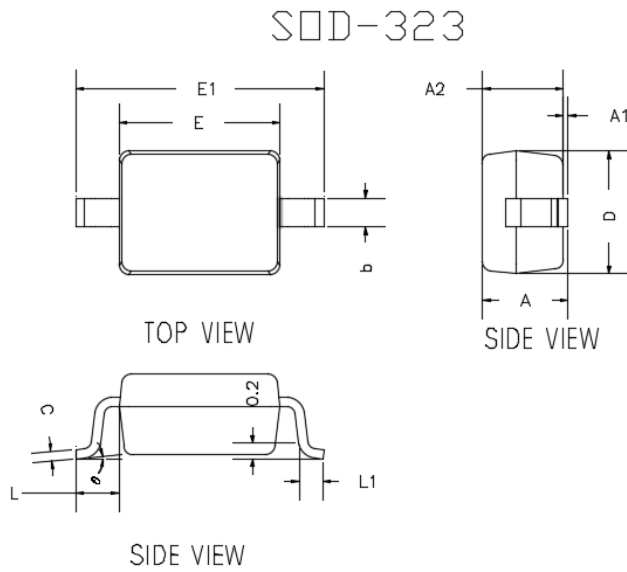
TLP Measurement





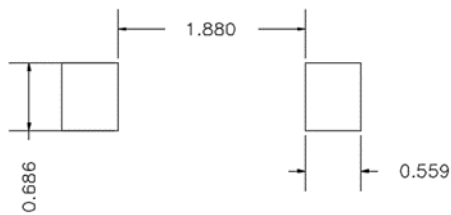
ESD48VD3B

■ Outline Dimensions



DIMENSIONS				
DIM	INCHES		MM	
	MIN	MAX	MIN	MAX
A	---	0.0393	---	1.0000
A1	0.0000	0.0039	0.0000	0.1000
A2	0.0314	0.0354	0.8000	0.9000
b	0.0098	0.0157	0.2500	0.4000
c	0.0031	0.0059	0.0800	0.1500
D	0.0472	0.0551	1.2000	1.4000
E	0.0629	0.0709	1.6000	1.8000
E1	0.0984	0.1063	2.5000	2.7000
L	0.0187TYP		0.475TYP	
L1	0.0098	0.0157	0.250	0.400
θ	0°	8°	0°	8°

■ Recommend land pattern (Unit:mm)



UNIT: mm

SUGGESTED SOLDER PAD LAYOUT

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met



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